Notice of Allowability	Application No.	Applicant(s)
	10/614,238	KOISHI, TAKAHIRO
	Examiner	Art Unit
	Alan S Chen	2182
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>application filed 07/08/2003</u> .		
2. X The allowed claim(s) is/are <u>1-10</u> .		
3. 🔯 The drawings filed on <u>08 July 2003</u> are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 1/30/04,10/3/03 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. Interview Summary Paper No./Mail Date Pape	e nent/Comment

Allowable Subject Matter

1. Claims 1-10 are allowed.

The following is the statement of reasons for the indication of allowable subject matter: The prior art disclosed by the applicant and cited by the Examiner fail to teach or suggest, alone or in combination, a system or method comprising at least on main processor, a sub processor connected to an I/O unit being operated by an operator; and a crossbar switch that transfers data between main and coprocessor, wherein the crossbar switch includes a diagnosis section which determines if a failure has occurred in main processor by using a command input from said I/O unit and given to said sub processor, the diagnosis section further capable of receiving and transmitting data to the main processor from and to said I/O unit via the coprocessor.

Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents are cited to further show the state of the art with respect to fault detection using a coprocessor:

U.S. Pat. No. 5,675,807 to Iswandhi et al. (hereafter Iswandhi). Iswandhi discloses a fault-tolerant system enclosed in a sub-system (Fig. 1A), wherein the fault tolerance is achieved using two processors, each processor being in lock-step. However, the diagnosis section is not completed at the coprocessor, but rather, the checking is done between the processor and memory (Column 5, lines 5-25) not the direct checking between the main processor and sub processor as claimed by applicant.

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U.S. Pat. No. 6,212,651 to Schieve et al. (hereafter Schieve). Schieve discloses a generalization of diagnostic coprocessors that can be used an indicator to report information about a computer system, e.g., intercepting a main CPU request and determining if it is faulty or not (Column 5, lines 43-67). However Schieve fails to disclose an input/output unit being operated by an operator that transmits data between the CPU and coprocessor or a crossbar switch and its connectivity between the main processor and sub processor

U.S. Pat. No. 6,233,702 to Horst et al. (hereafter Horst). Horst discloses a fault-tolerant system enclosed in a sub-system (Fig. 1A), wherein the fault tolerance is achieved using two processors, each processor being in lock-step. However, the diagnosis section is not completed at the coprocessor, but rather, the checking is done between the processor and memory (Column 5, lines 5-25).

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alan S Chen whose telephone number is 571-272-4143. The examiner can normally be reached on M-F 8:30am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A Gaffin can be reached on (571) 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ASC 3/14/2005